



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Bachelor of Technology (Electrical & Electronics Engineering)**  
**Choice Based Credit System (CBCS)**


**SEMESTER - VII (w.e.f 2017-18)**


S. No.	COURSE CODE	COURSE NAME	TEACHING			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEE701	Electric Drives	3	1	2	5	60	20	20	30	20	150
2	BTEE702	Electrical Machine Design	3	1	2	5	60	20	20	30	20	150
3	BTEE703	Major Project	0	0	8	4	0	0	0	60	40	100
4	BTEE704	Software Simulation Lab	0	0	2	1	0	0	0	30	20	50
5	BTEE705	Electrical Engineering Drawing	0	0	2	1	0	0	0	0	50	50
6		Elective-I	3	0	0	3	60	20	20	0	0	100
7		Elective-II	3	0	0	3	60	20	20	0	0	100
8	BBAI501	Human Values and Professional Ethics	4	0	0	4	60	20	20	0	0	100
			16	2	16	26	300	100	100	150	150	800

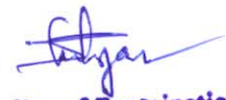
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

Elective-I		Elective-II	
BTEE711	High Voltage Engineering	BTEE721	Digital Signal Processing
BTEE712	Extra High Voltage Alternating Current and Direct Current Transmission	BTEE722	Supervisory Control and Data Acquisition systems and Applications
BTEE713	Power Plant Instrumentation and Control	BTEE723	Artificial Neural Network

\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

  
**Chairperson**  
**Board of Studies**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya  
 Indore

  
**Chairperson** 25/08/2020  
 Faculty of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, INDORE

  
**Controller of Examinations**  
 Shri Vaishnav Vidyapeeth Vishwavidyalaya

  
**Registrar**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

  
**Vice Chancellor**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore